



1/4

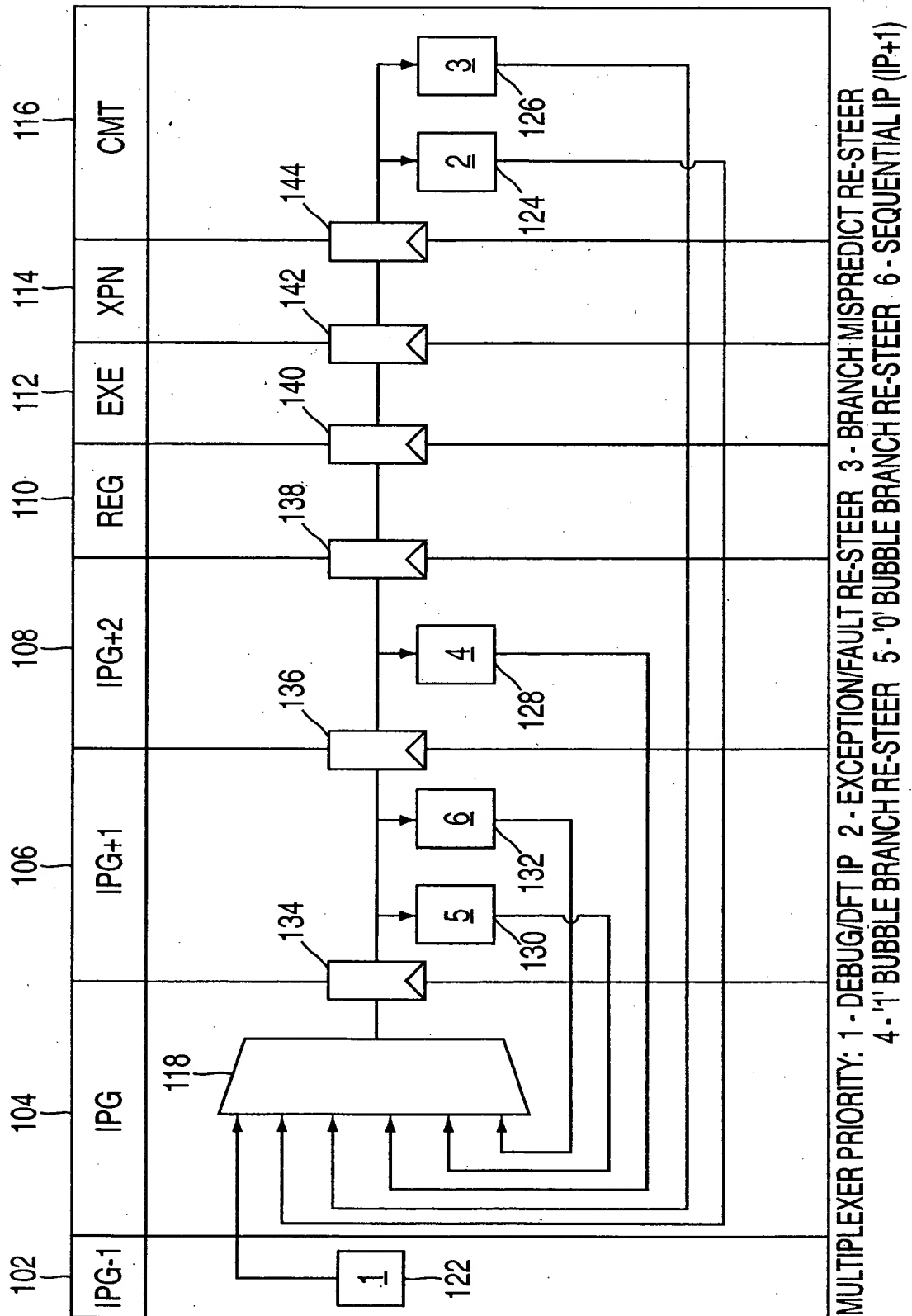
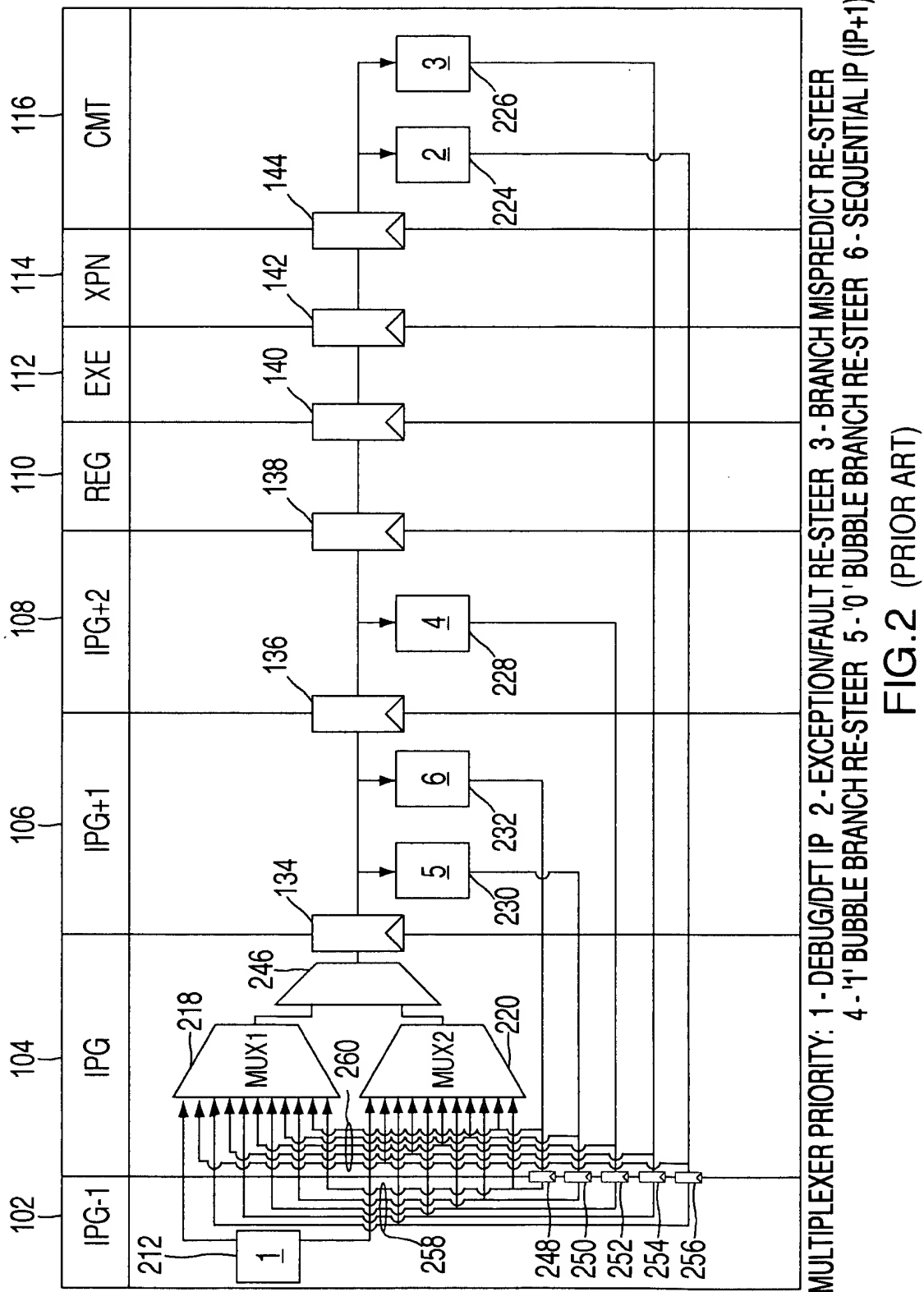
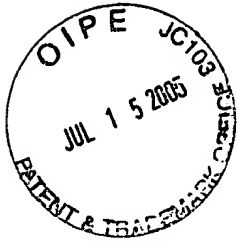
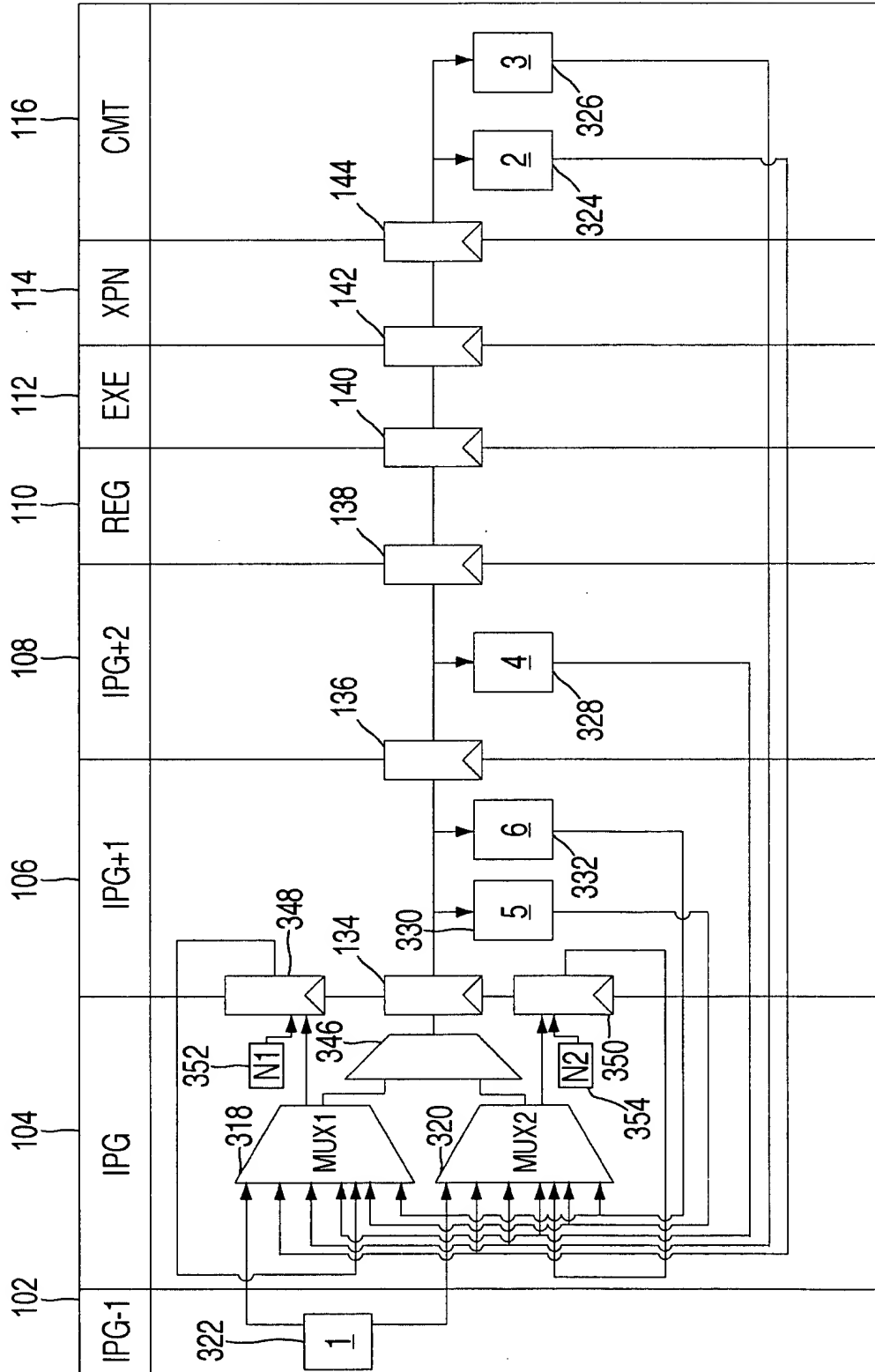


FIG.1 (PRIOR ART)





MULTIPLEXER PRIORITY: 1 - DEBUG/DFT IP 2 - EXCEPTION/FAULT RE-STEER 3 - BRANCH MIS-PREDICT RE-STEER
 4 - '1' BUBBLE BRANCH RE-STEER 5 - '0' BUBBLE BRANCH RE-STEER 6 - SEQUENTIAL IP (IP+1)
 FIG. 3



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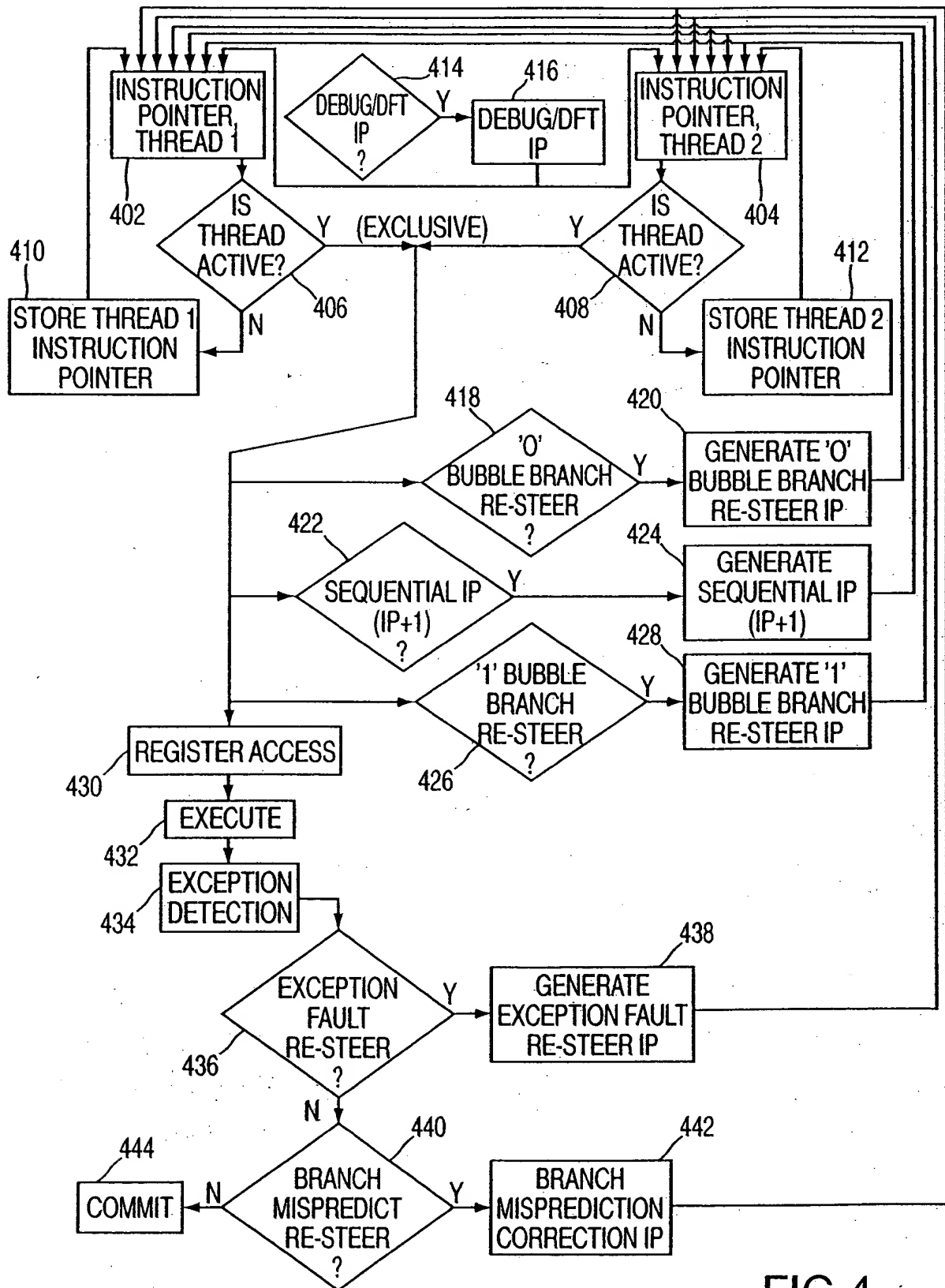


FIG.4